

L Number	Hits	Search Text	DB	Time stamp
1	47057	sram or (static adj5 random adj5 access adj5 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:46
2	19882	(sram or (static adj5 random adj5 access adj5 memory)) and transistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:46
3	258	((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:48
6	21	((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM) and 365/156.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:58
11	16318	(sram or (static adj5 random adj5 access adj5 memory)) and "memory device"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:52
12	8656	((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:52
13	3	((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and ("four\$1transistor" or 4T\$1SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:53
14	3	((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and 4T\$1SRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:54
15	75	((sram or (static adj5 random adj5 access adj5 memory)) and "memory device") and transistor) and (((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/\$7.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:54
16	0	chou-chung.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55
17	29	chou-chung-\$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55
18	2	chung-chou.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55
19	0	chung-chou-\$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/01 01:55

20	0	chou-chung-c.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:55
21	12	"four\$1transistor" or 4T\$1SRAM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:57
4	137	((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/\$7.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:57
9	2	((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/191.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:58
10	4	((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/226.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:58
5	48	((sram or (static adj5 random adj5 access adj5 memory)) and transistor) and ("four-transistor" or 4T-SRAM)) and 365/154.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/01 01:58